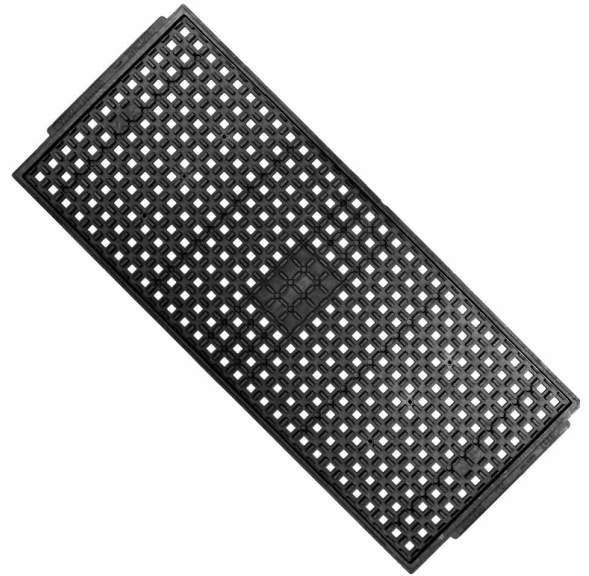


PRODUCT

Waffle Trays - BGA

TECHNICAL DATASHEET



DESCRIPTION

Manufactured to meet stringent JEDEC global standards, the waffle tray design enables integrated circuits to be handled safely and securely during the various stages of manufacturing, testing, and shipping.

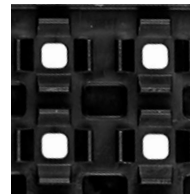
All JEDEC waffle trays have the same “X” and “Y” dimensions and are easily stacked for storage and transportation.

APPLICATIONS

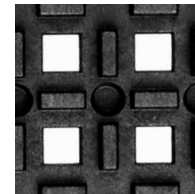
- Assembly and testing
- Shipping and storage
- Manufacturing processes
- Pick and place automation

CELL STYLES

030-1278



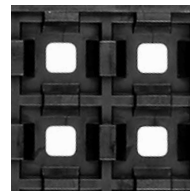
030-0064



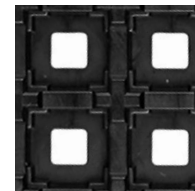
030-0065



030-0113



030-1194



030-1293



PRODUCT CODE	CELL SIZE (mm)	MATRIX	USABLE CELLS	MAX HEAT (°C)	QUANTITY
030-1278	7 x 7	10 x 26	232	150	Each
030-0064	8 x 8	12 x 29	332	150	Each
030-0065	8 x 13	11 x 19	200	150	Each
030-0113	9 x 9	10 x 26	236	150	Each
030-1194	10 x 10	10 x 24	216	150	Each
030-1293	11 x 11	8 x 22	170	150	Each

To request a quotation or for more information, please call **+44 (0)1473 836200**
email info@antistat.co.uk or visit www.antistat.co.uk

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