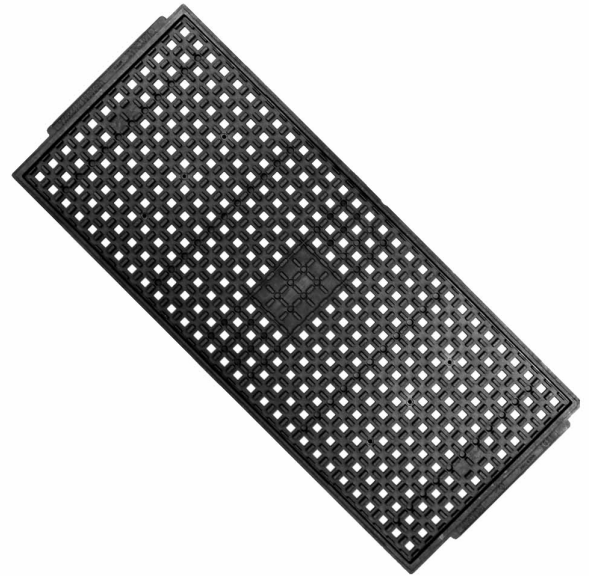


PRODUCT

Waffle Trays - QFN

TECHNICAL DATASHEET



DESCRIPTION

Manufactured to meet stringent JEDEC global standards, the waffle tray design enables integrated circuits to be handled safely and securely during the various stages of manufacturing, testing, and shipping.

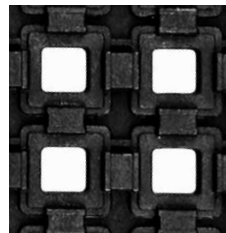
All JEDEC waffle trays have the same “X” and “Y” dimensions and are easily stacked for storage and transportation.

APPLICATIONS

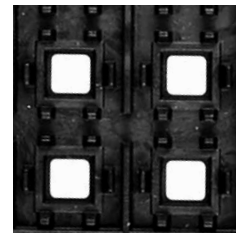
- Assembly and testing
- Shipping and storage
- Manufacturing processes
- Pick and place automation

CELL STYLES

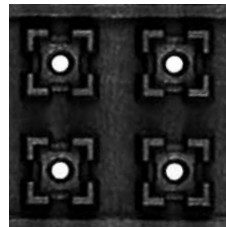
030-0118



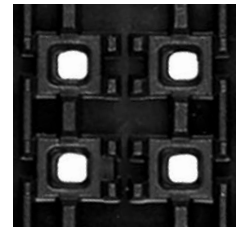
030-0155



030-0157



030-0160



PRODUCT CODE	CELL SIZE (mm)	MATRIX	USABLE CELLS	MAX HEAT (°C)	QUANTITY
030-0118	3 x 3 x 1.0	14 x 35	438	140	Each
030-0155	5 x 5 x 1.0	14 x 35	436	150	Each
030-0157	7 x 7 x 1.0	10 x 26	240	150	Each
030-0160	8 x 8 x 1.0	10 x 26	232	150	Each

To request a quotation or for more information, please call **+44 (0)1473 836200**
email info@antistat.co.uk or visit www.antistat.co.uk

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